

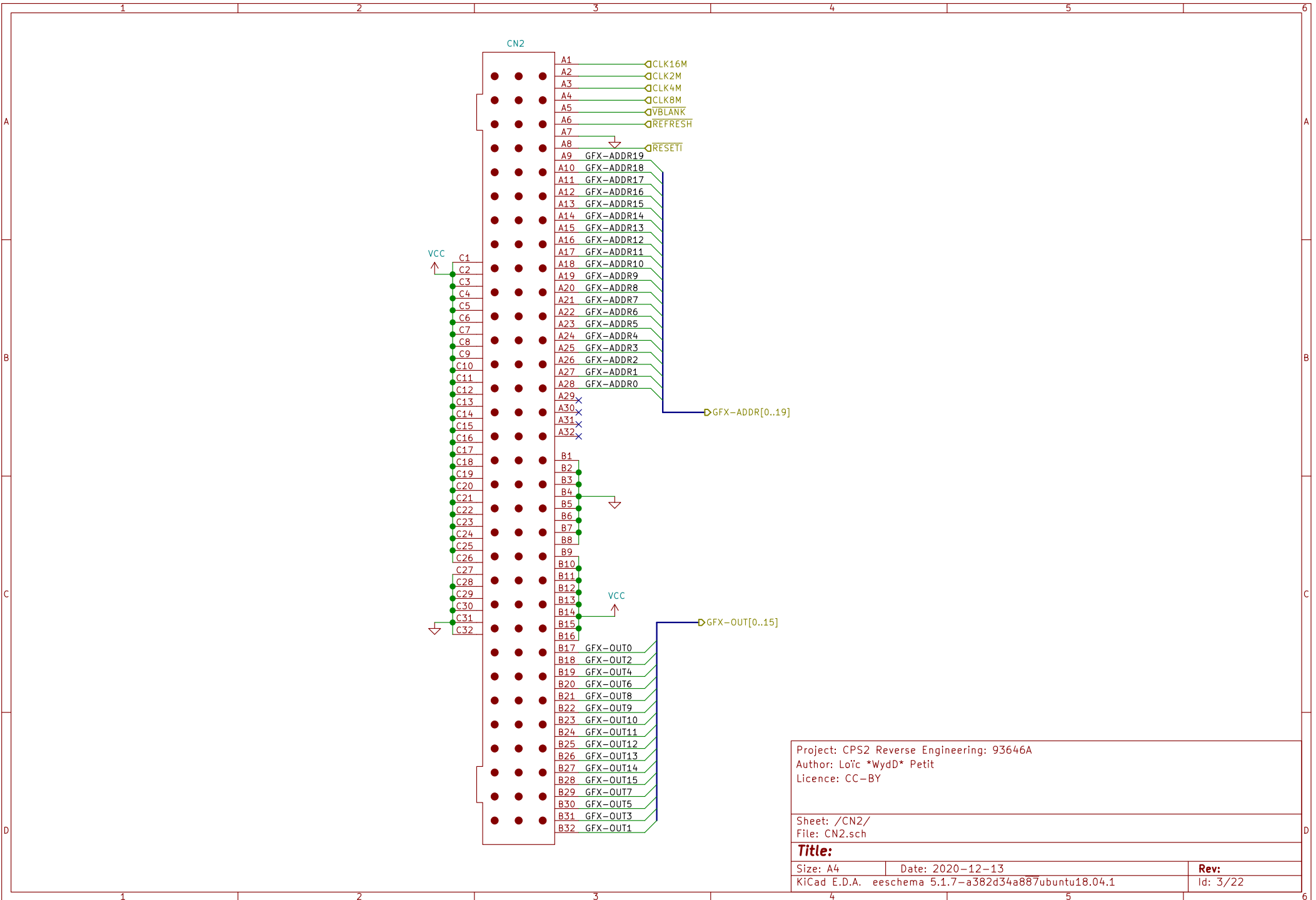
Project: CPS2 Reverse Engineering: 93646A  
 Author: Loïc \*WydD\* Petit  
 Licence: CC-BY

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 File: CN1.sch

**Title:**

Size: A4 Date: 2020-12-13  
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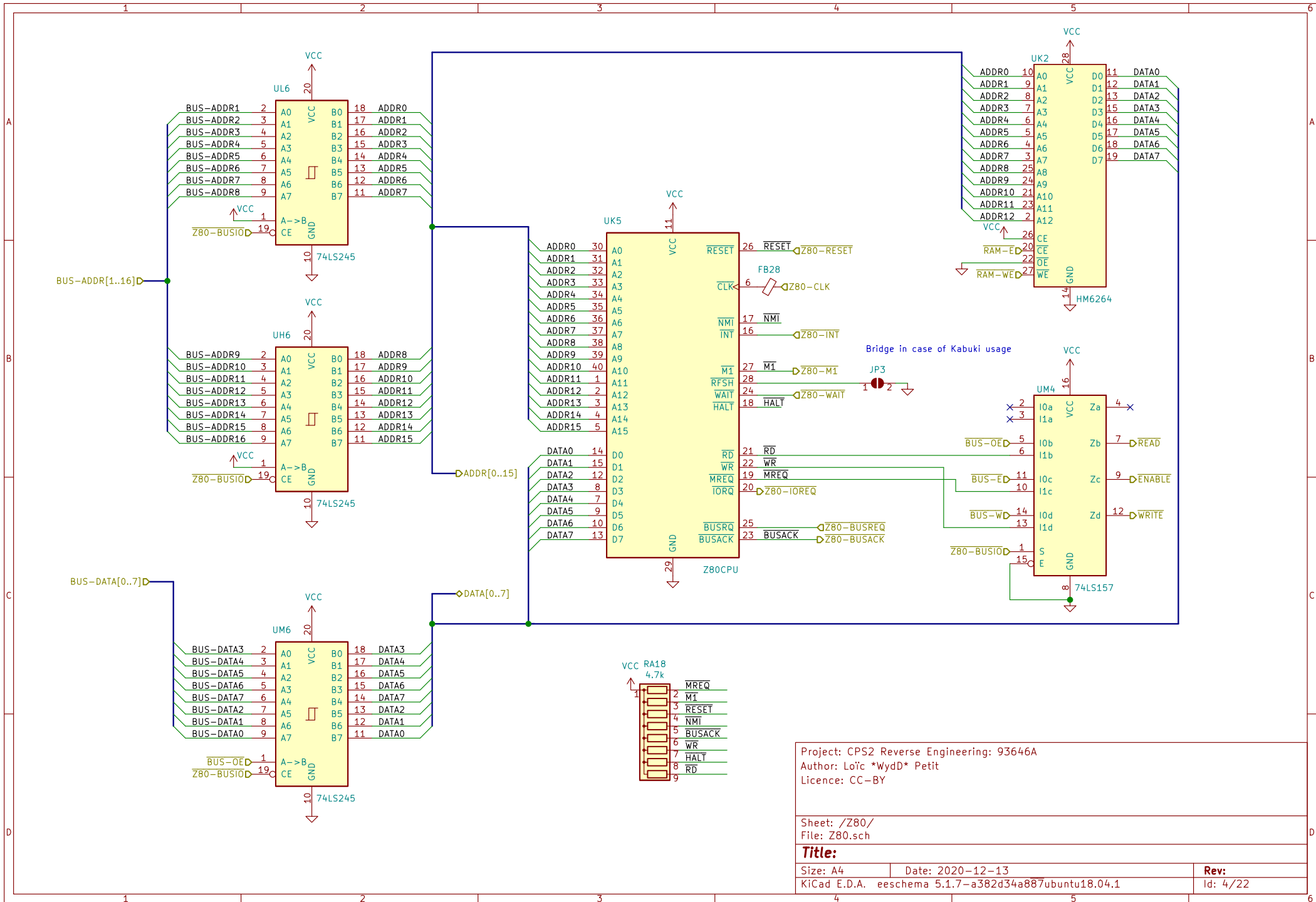
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Rev:	Id: 3/22



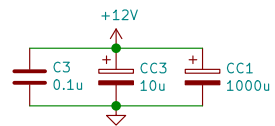
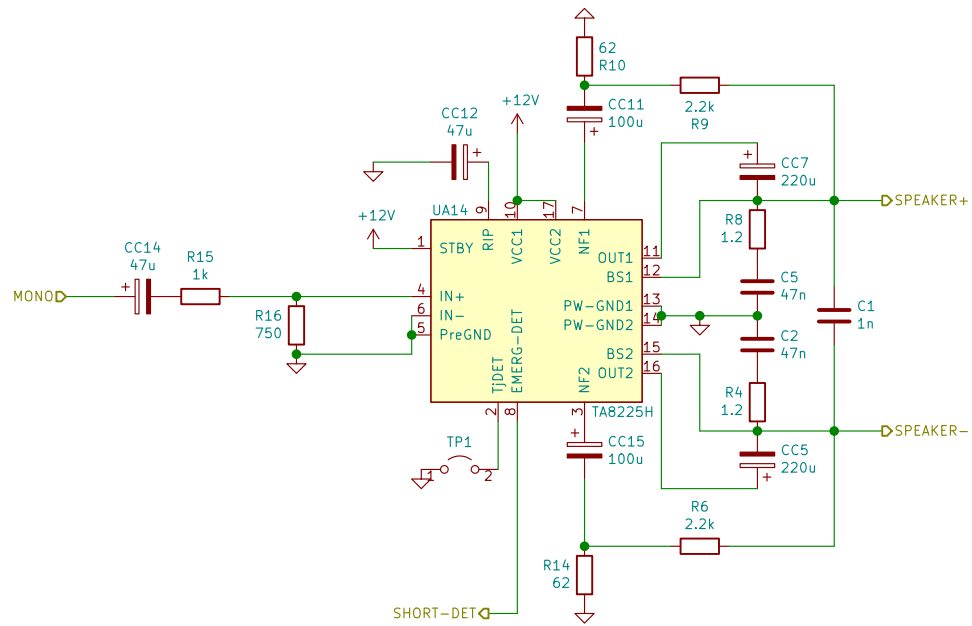
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**Title:**

Size: A4 Date: 2020-12-13  
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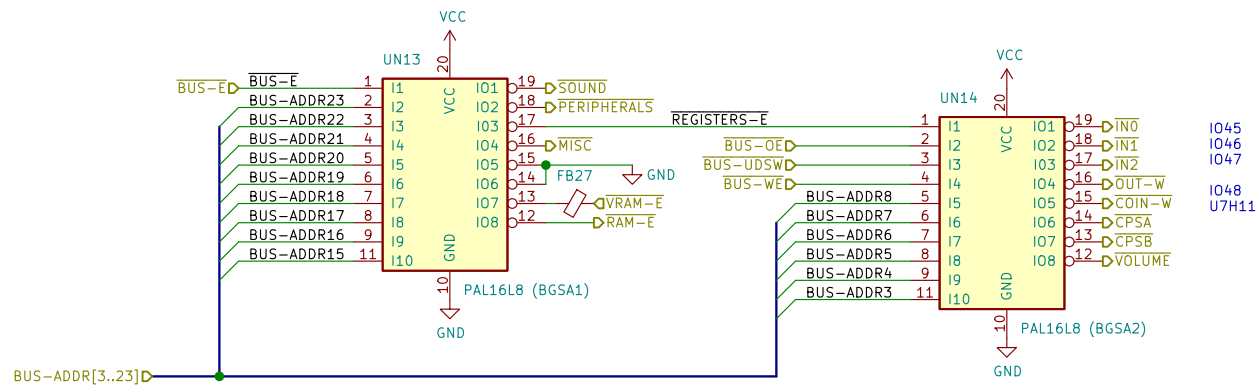
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**Title:**

Size: A4 Date: 2020-12-13  
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```

/o12 = /i1 & i2 & i3 & i4 & i5 & i6 & i7 & i8 & i9 & /i14 & /i15 + /i13
/o16 = /i1 & /i2 & i3 & i4 & /i5 & /i6 & i7 & i8
/o17 = /i1 & i2 & /i3 & /i4 & /i5 & /i6
/o18 = /i1 & /i2 & i3 & i4 & /i5 & /i6 & /i7 & i8
/o19 = /i1 & /i2 & i3 & i4 & /i5 & /i6 & /i7 & /i8

```

```

/o12 = /BUS-E & ADDR=FFxxx + VRAM-E // RAM (Work + Video)
/o16 = /BUS-E & ADDR=(660000 > 67ffff) // Peripherals
/o17 = /BUS-E & ADDR=(800000 > 87ffff) // Registers
/o18 = /BUS-E & ADDR=(620000 > 63ffff) // ?? unused
/o19 = /BUS-E & ADDR=(600000 > 61ffff) // QSound RAM
ADDR is in 8 bits to compare to MAME source

```

```

/o12 = /i1 & /i2 & /i5 & /i6 & /i7 & i8 & i9 & /i11
/o13 = /i1 & i5 & /i6 & i7
/o14 = /i1 & i5 & /i6 & /i7
/o15 = /i1 & /i4 & /i5 & /i6 & i7 & /i8 & /i9 & /i11
/o16 = /i1 & /i3 & /i5 & /i6 & i7 & /i8 & /i9 & /i11
/o17 = /i1 & /i2 & /i5 & /i6 & /i7 & i8 & /i9 & /i11
/o18 = /i1 & /i2 & /i5 & /i6 & /i7 & /i8 & i9 & /i11
/o19 = /i1 & /i2 & /i5 & /i6 & /i7 & /i8 & /i9 & /i11

```

```

/o12 = /OE & ADDR.endsWith(30 - 37) // qsound volume
/o13 = ADDR.endsWith(140 - 17F) // cps_b_regs
/o14 = ADDR.endsWith(100 - 13F) // cps_a_regs
/o15 = /WE & ADDR.endsWith(40 - 47) // coin lock
/o16 = CONTROL1 & ADDR.endsWith(40 - 47) // eeprom
/o17 = /OE & ADDR.endsWith(20 - 27) // IN2
/o18 = /OE & ADDR.endsWith(10 - 17) // IN1
/o19 = /OE & ADDR.endsWith(00 - 07) // IN0

```

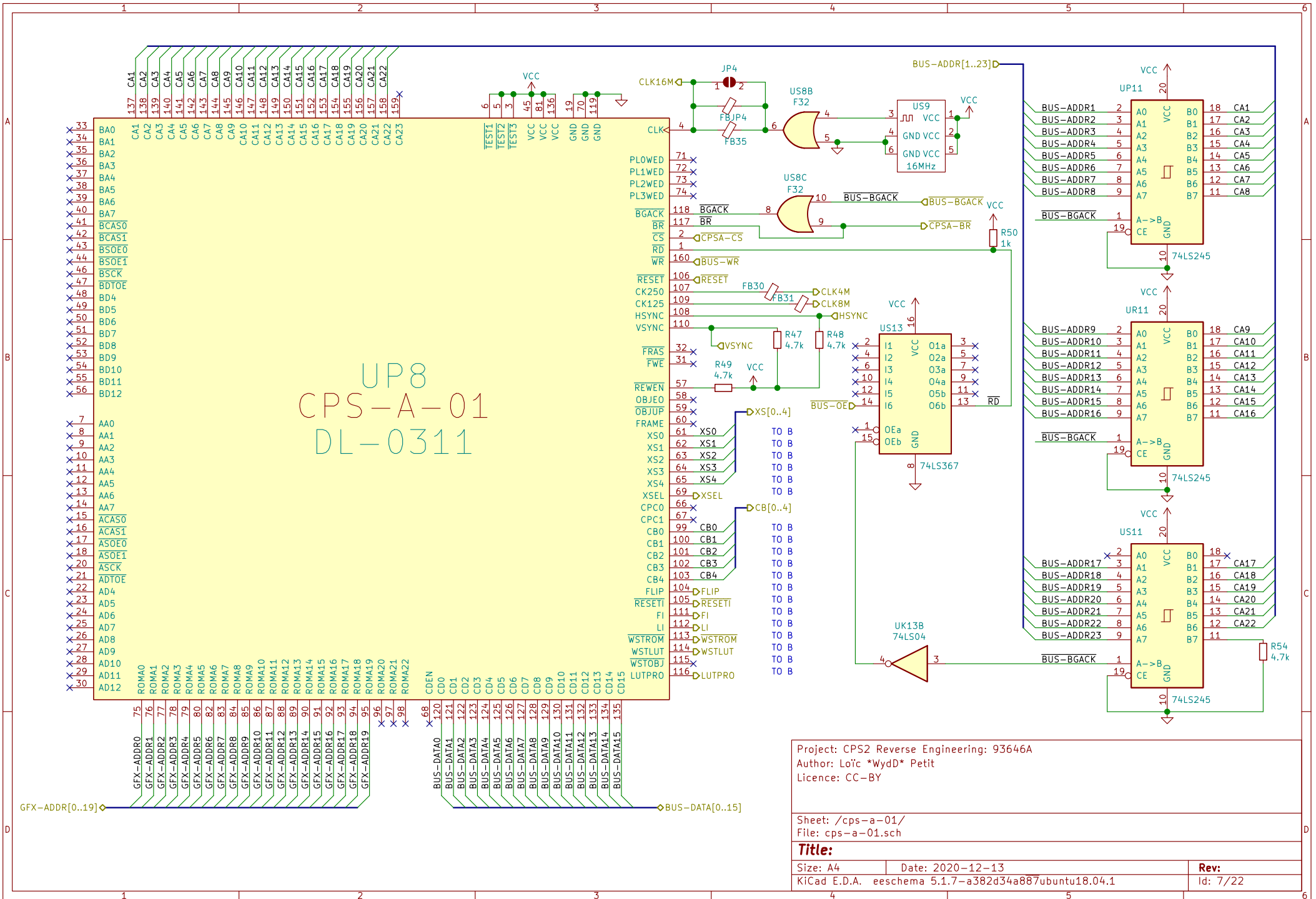
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Size: A4 Date: 2020-12-13  
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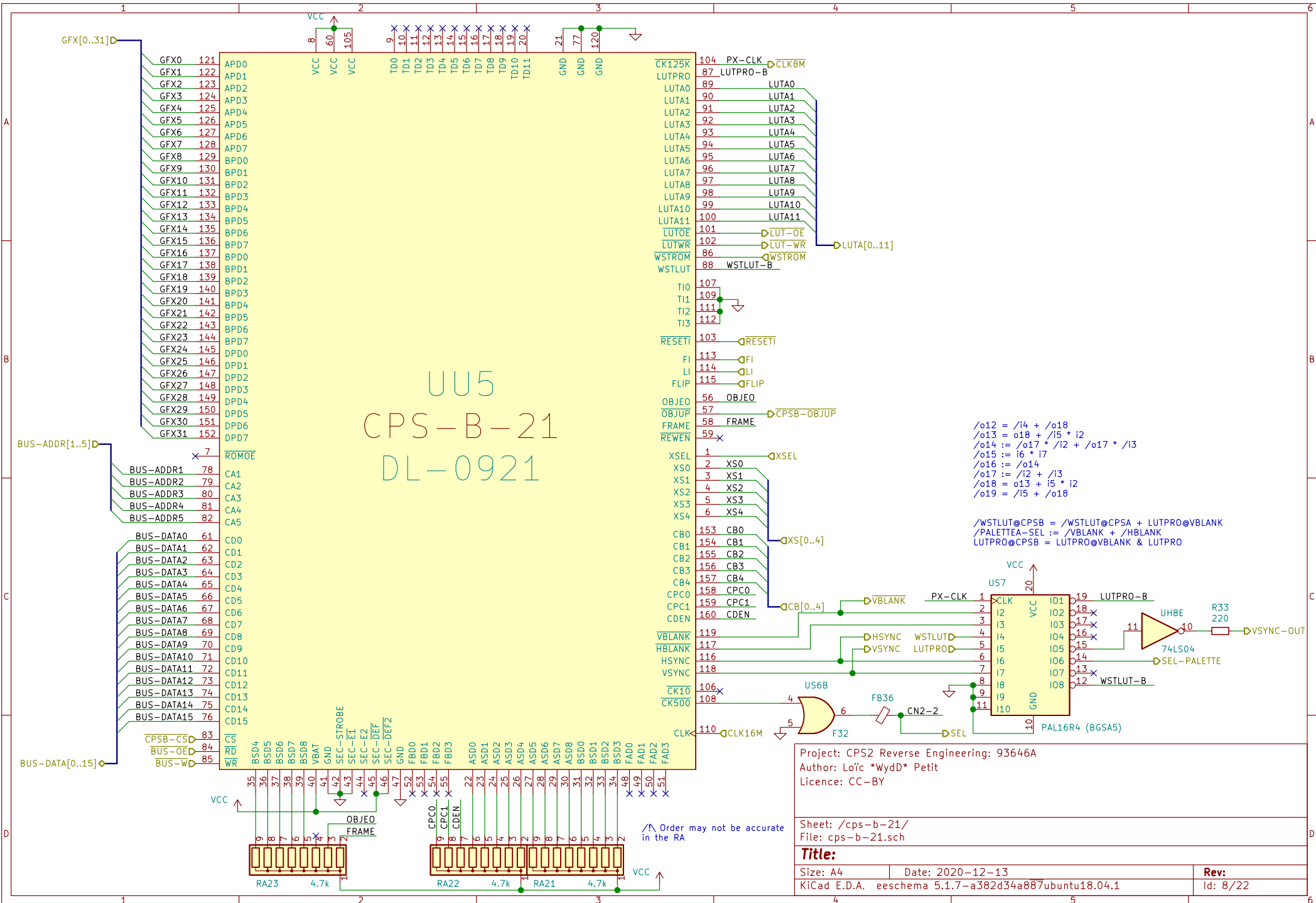
Project: CPS2 Reverse Engineering: 93646A  
 Author: Loïc \*Wyd\* Petit  
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Size: A4  
 Date: 2020-12-13  
 KiCad E.D.A. eeschema 5.1.7-a382d34a887ubuntu18.04.1

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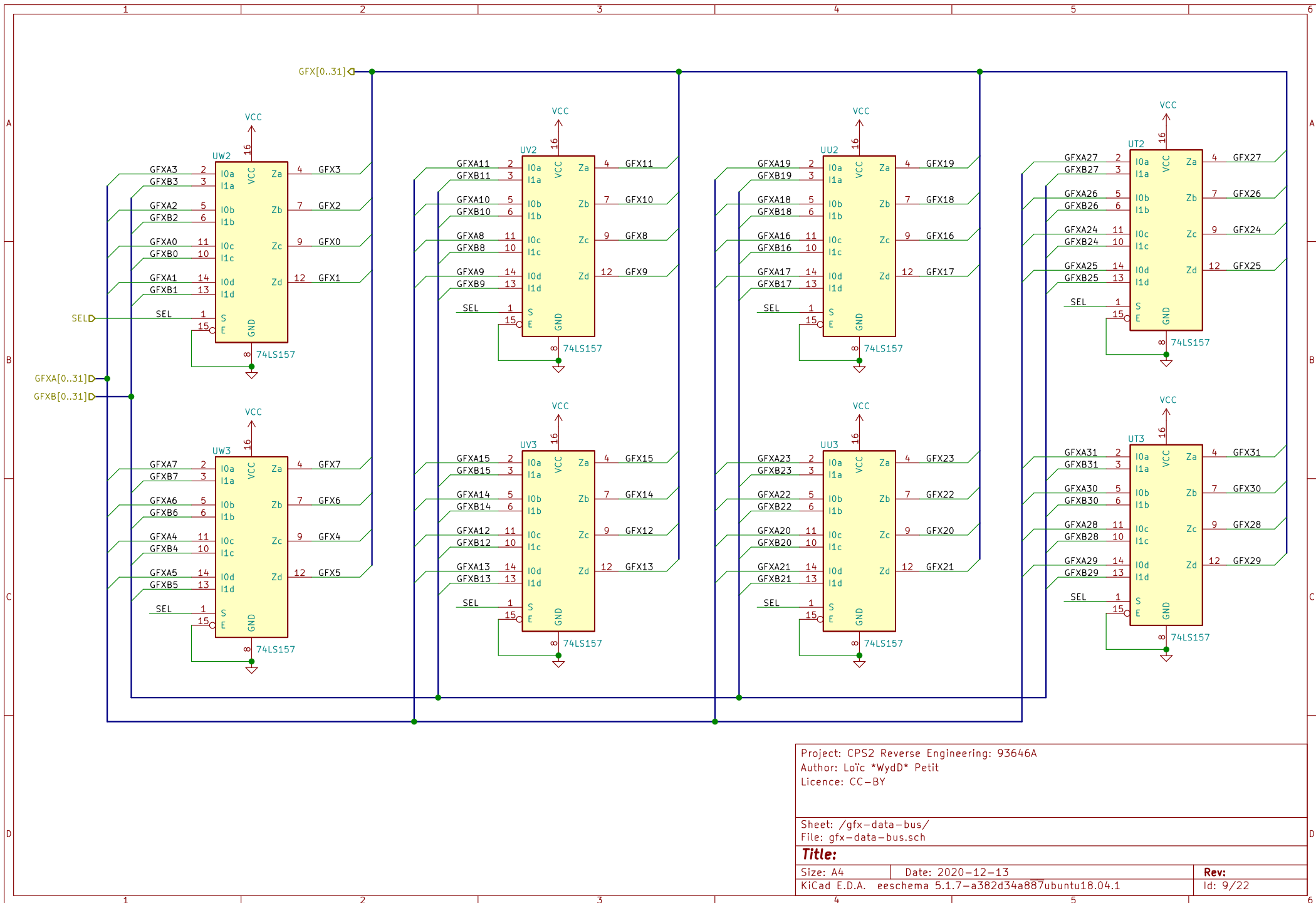
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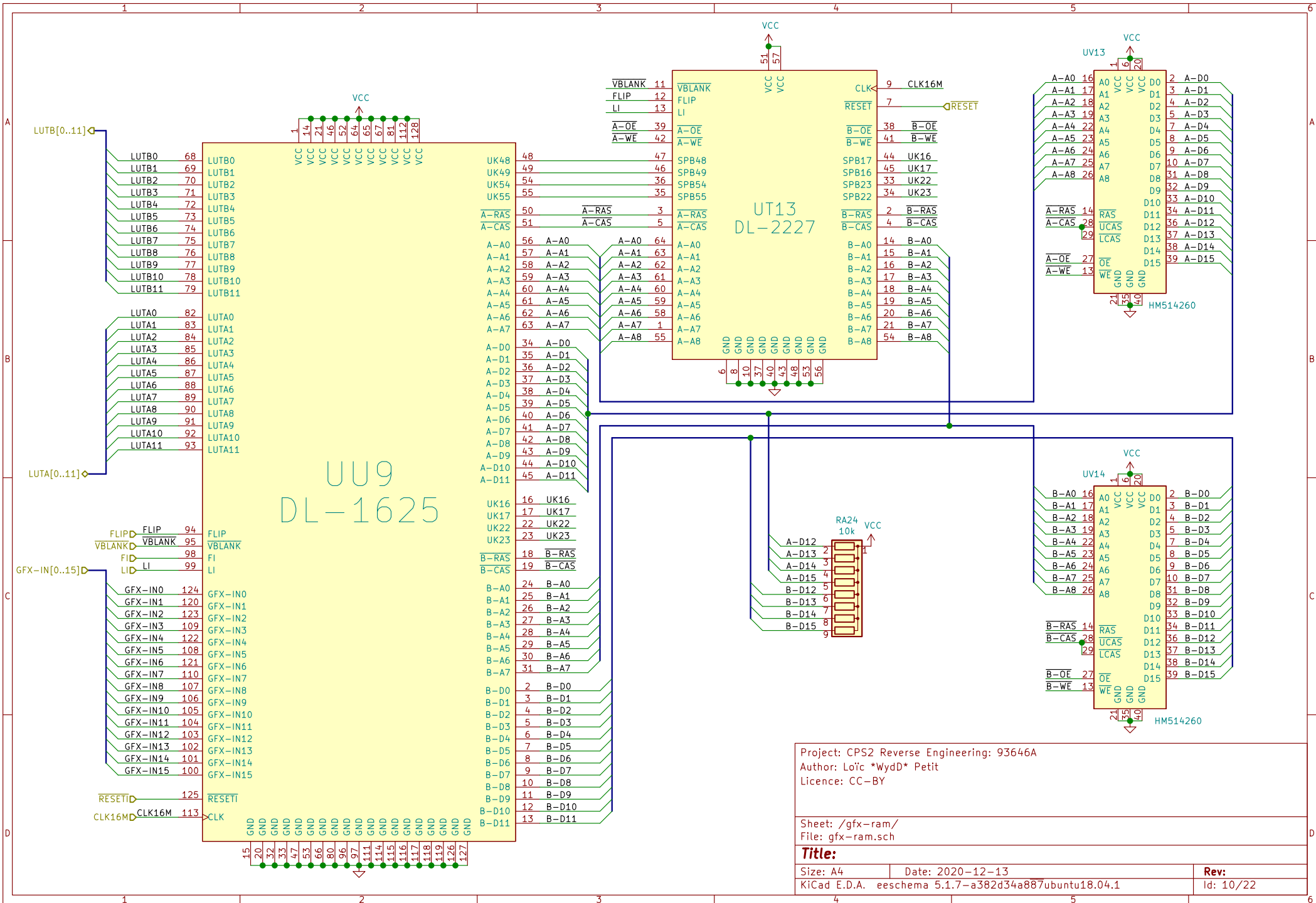
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Date: 2020-12-13

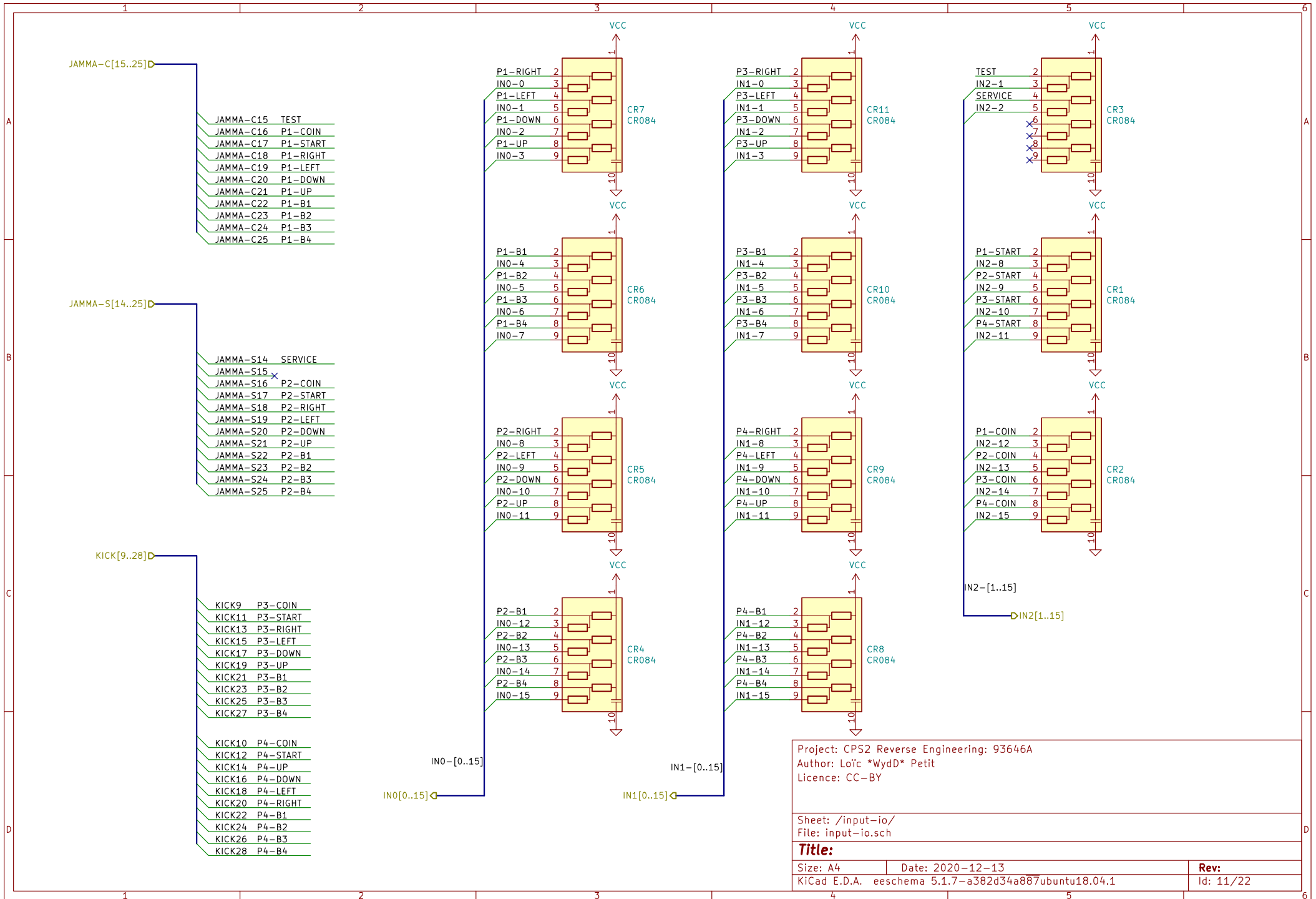
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Rev:	Id: 10/22



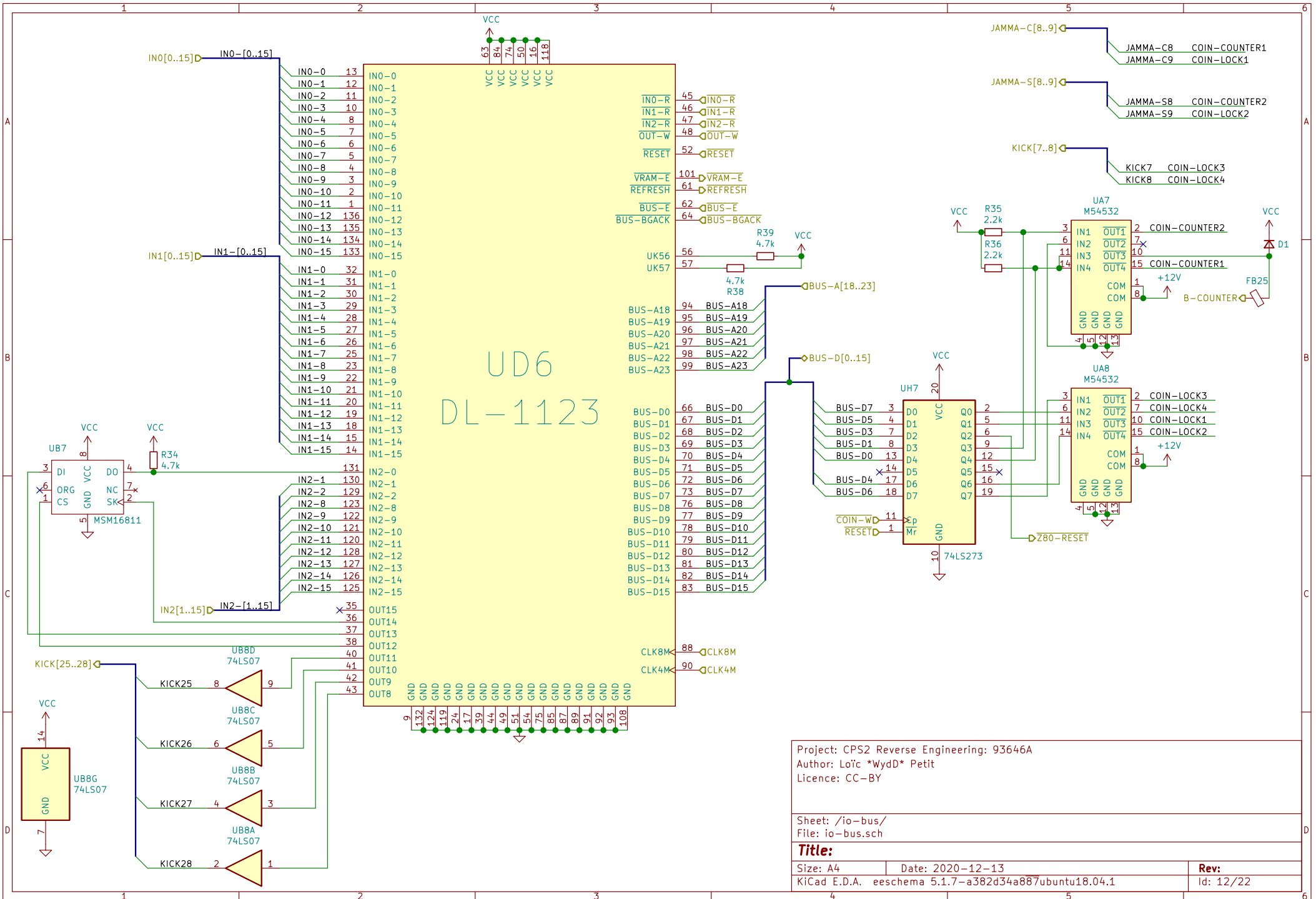
Project: CPS2 Reverse Engineering: 93646A  
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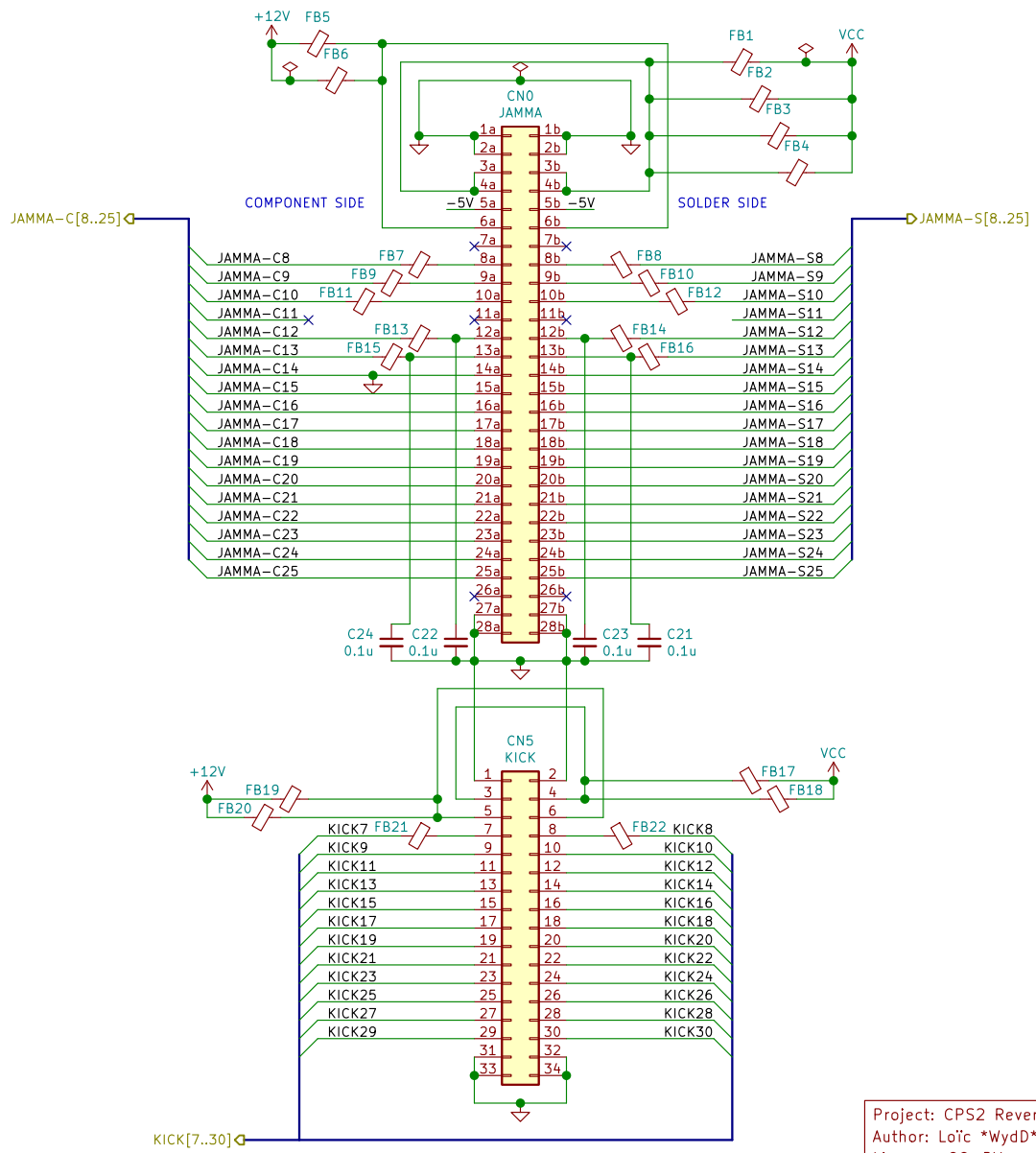
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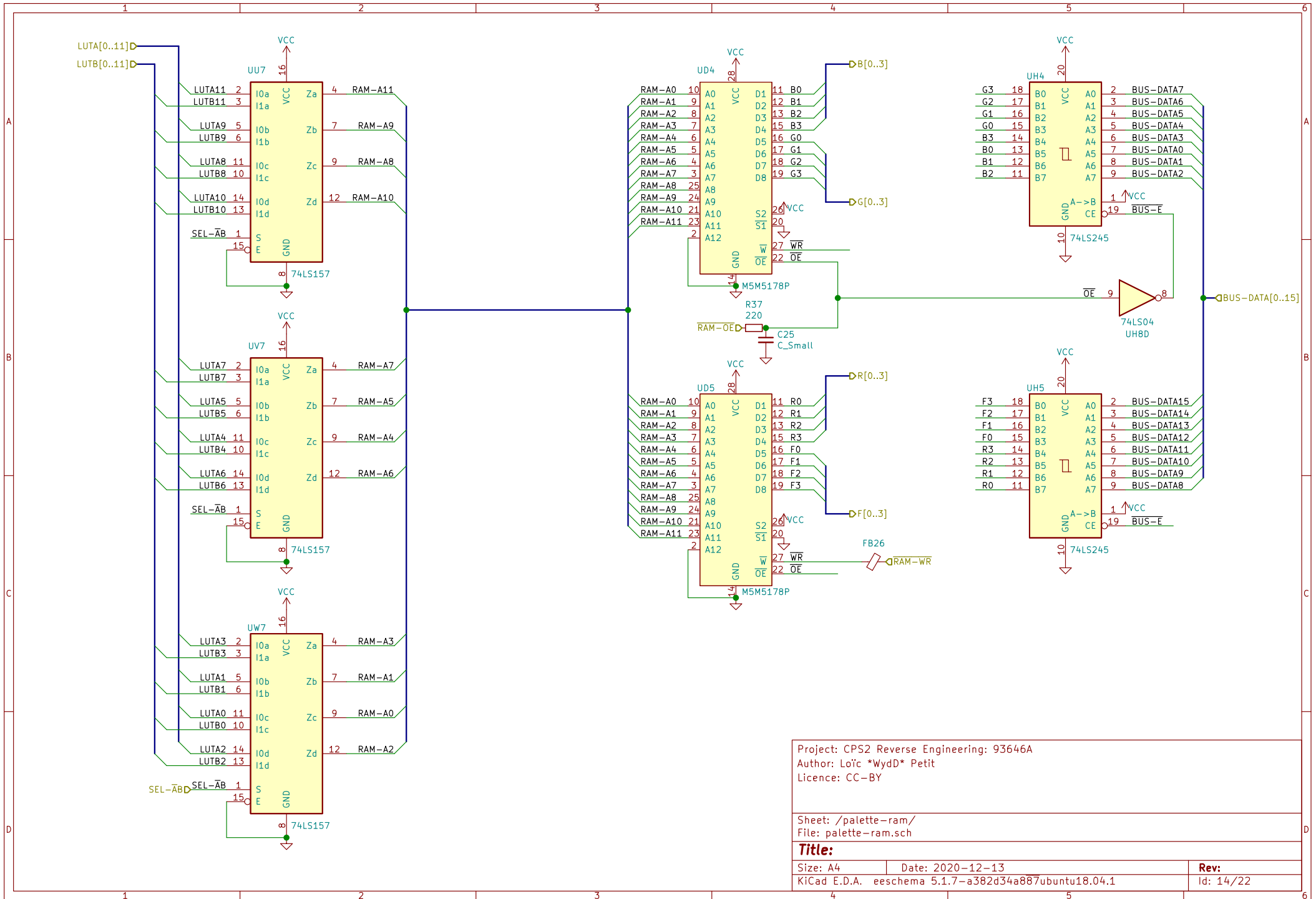
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Sheet: /jamma/  
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KiCad E.D.A. eeschema 5.1.7-a382d34a887ubuntu18.04.1		Id: 13/22



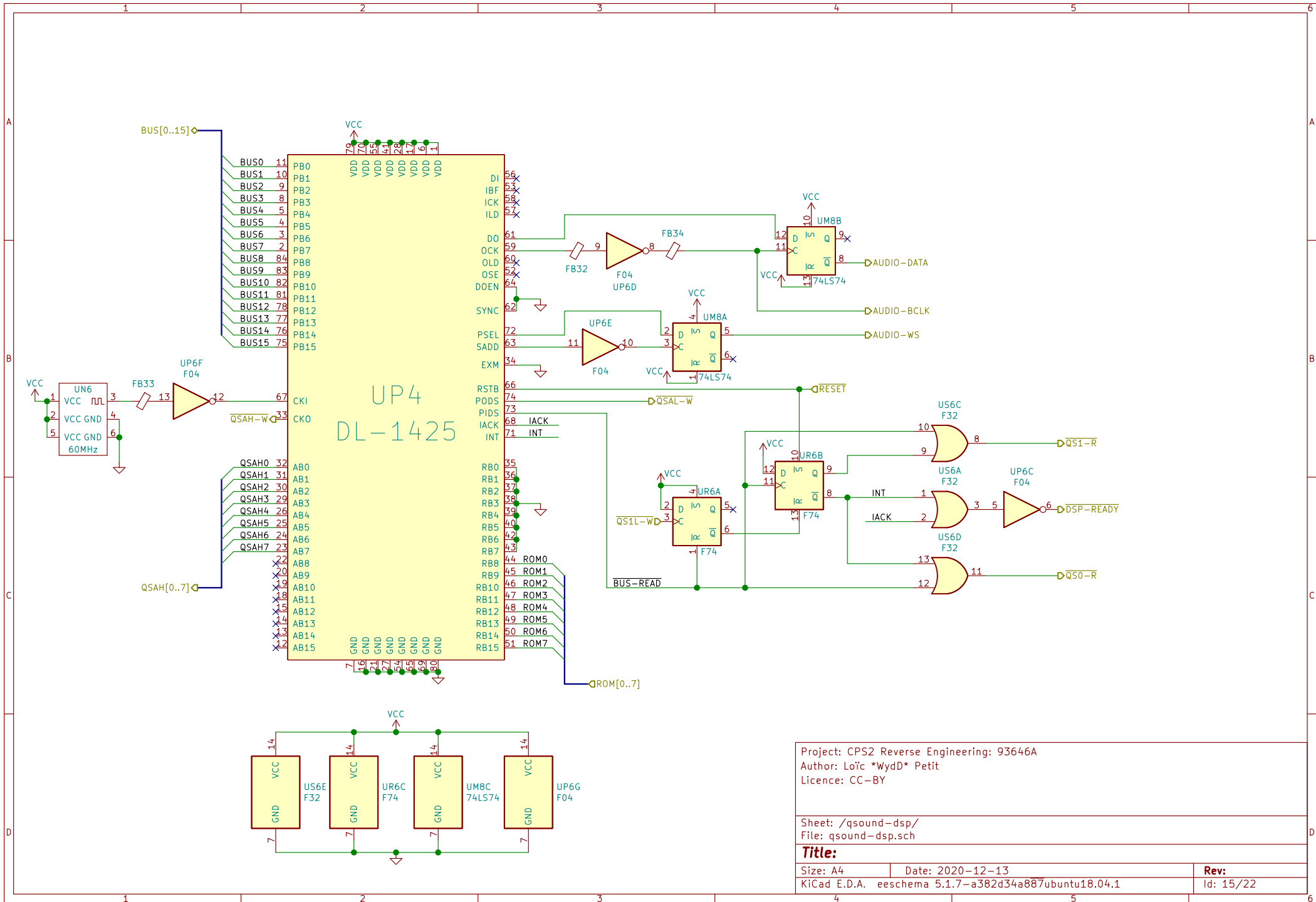
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 Author: Loïc \*WydD\* Petit  
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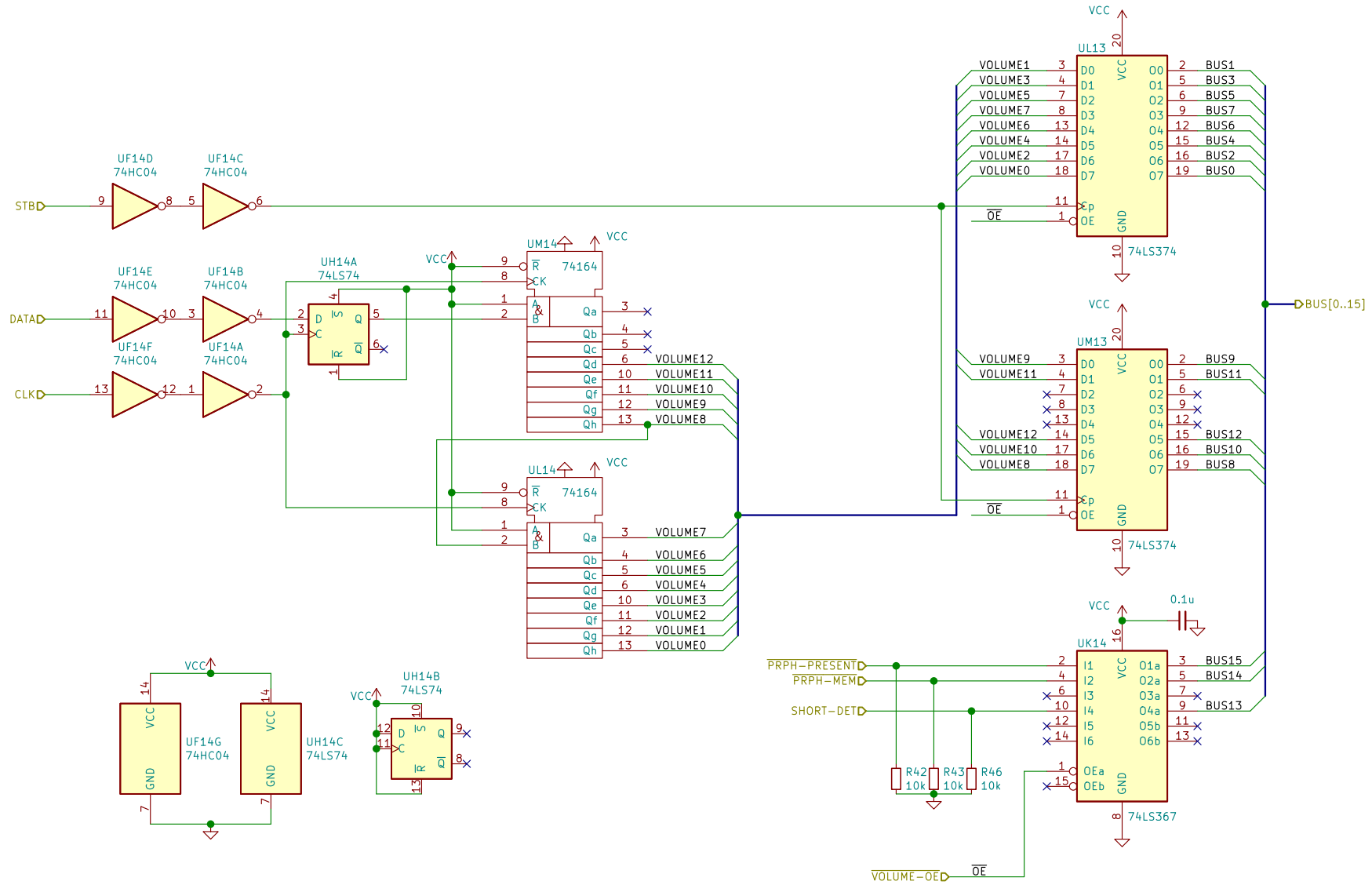
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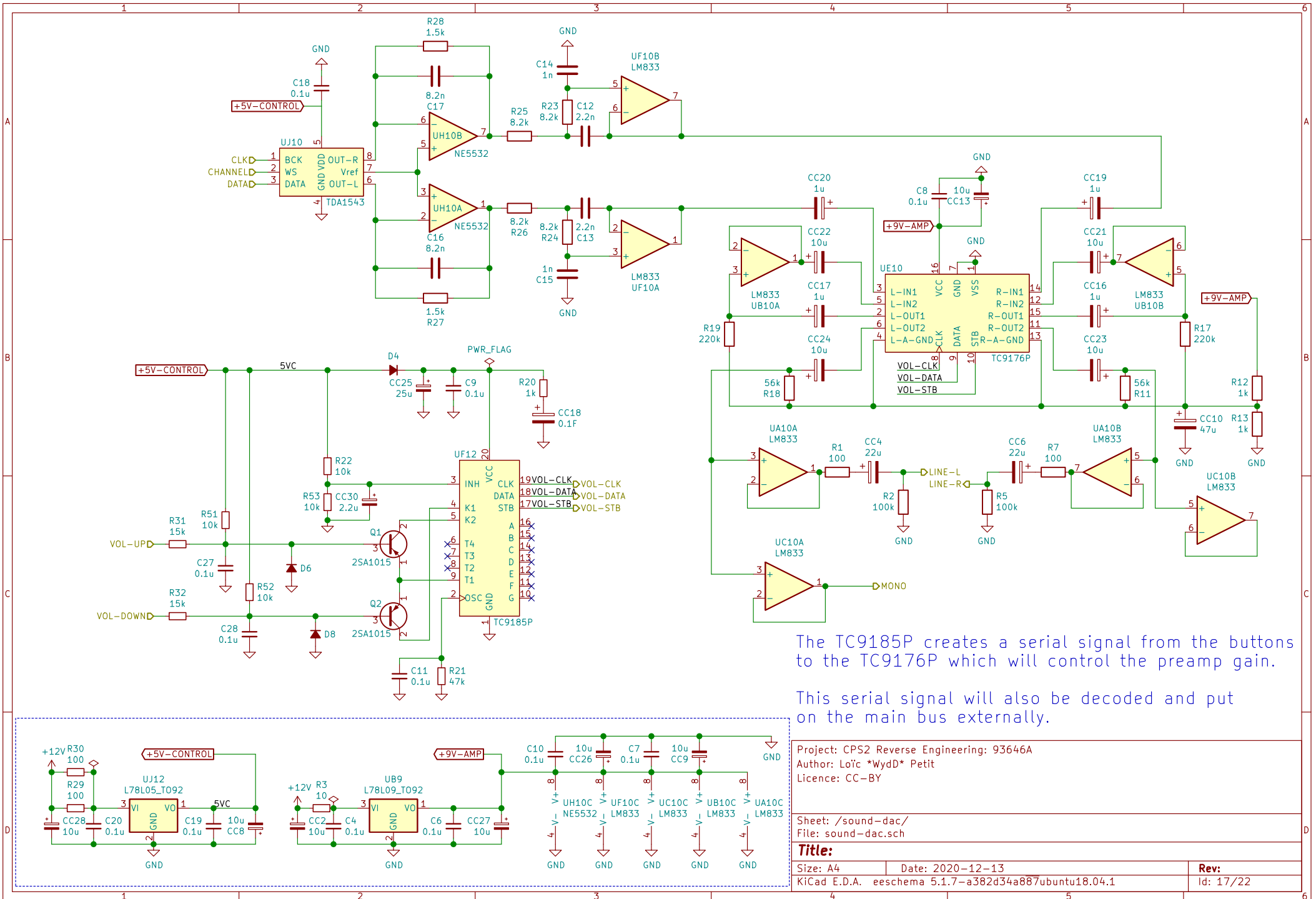
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**Title:**

Size: A4 Date: 2020-12-13  
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Rev:  
 Id: 16/22





The TC9185P creates a serial signal from the buttons to the TC9176P which will control the preamp gain.

This serial signal will also be decoded and put on the main bus externally.

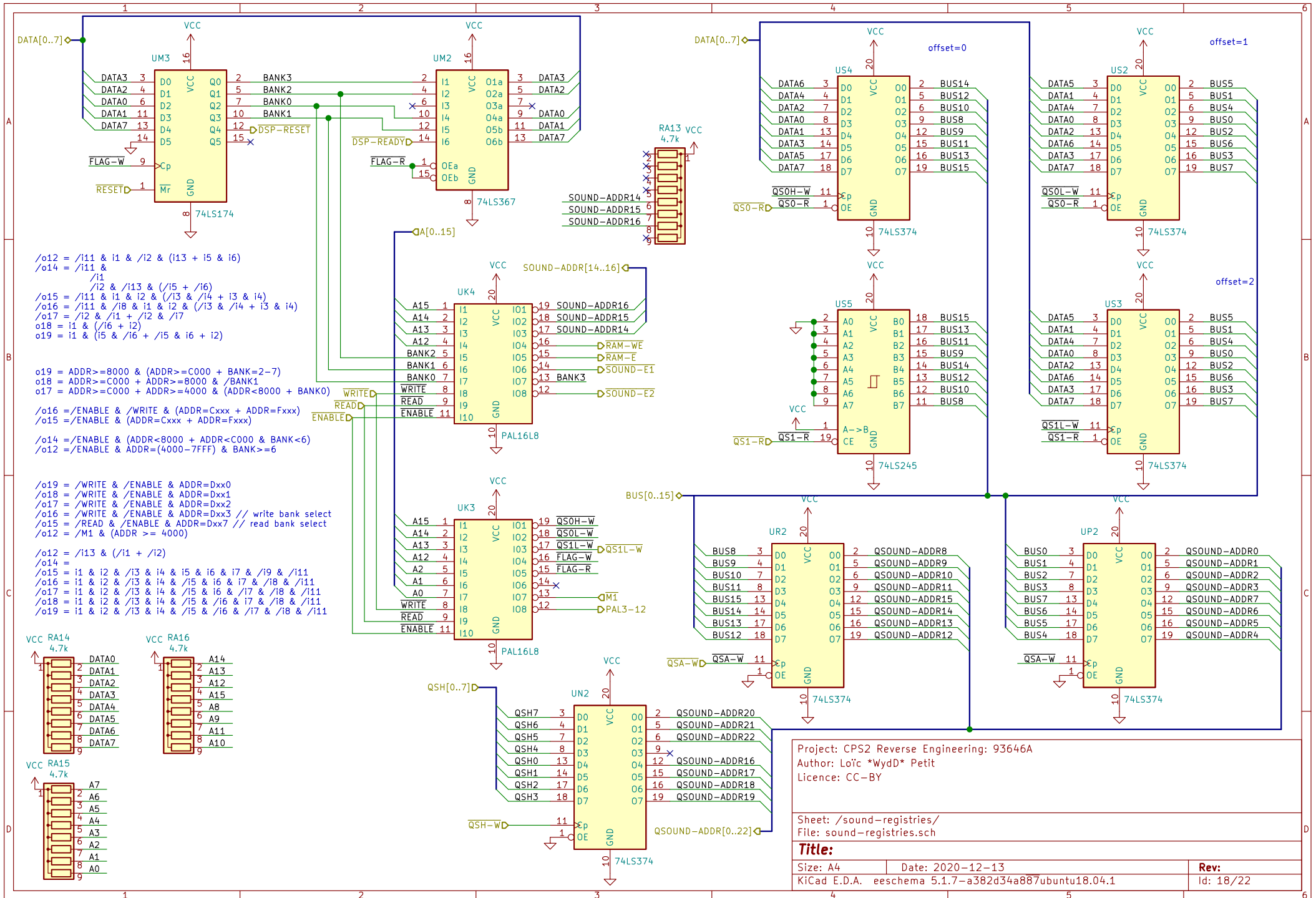
Project: CPS2 Reverse Engineering: 93646A  
 Author: Loïc \*WydD\* Petit  
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Size: A4 Date: 2020-12-13  
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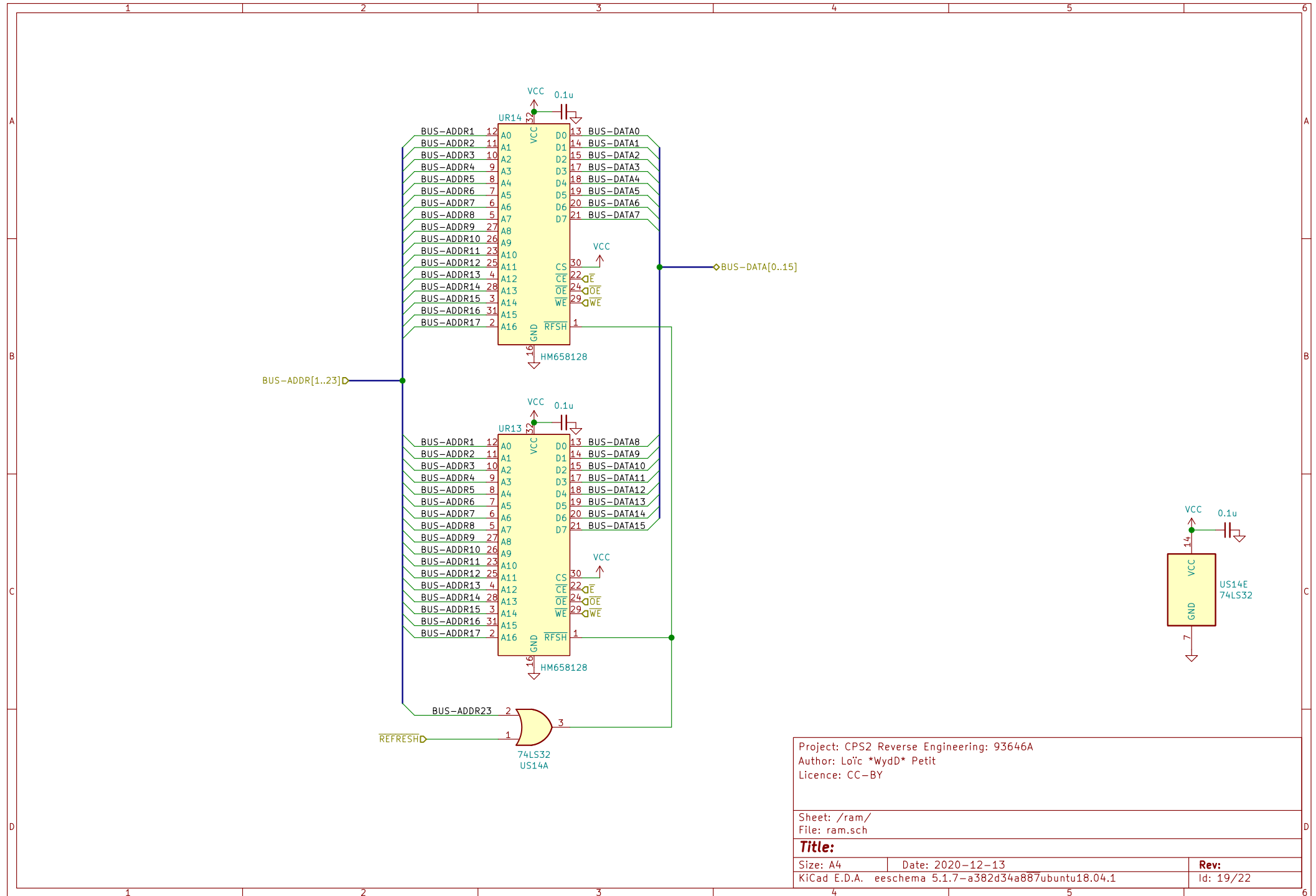
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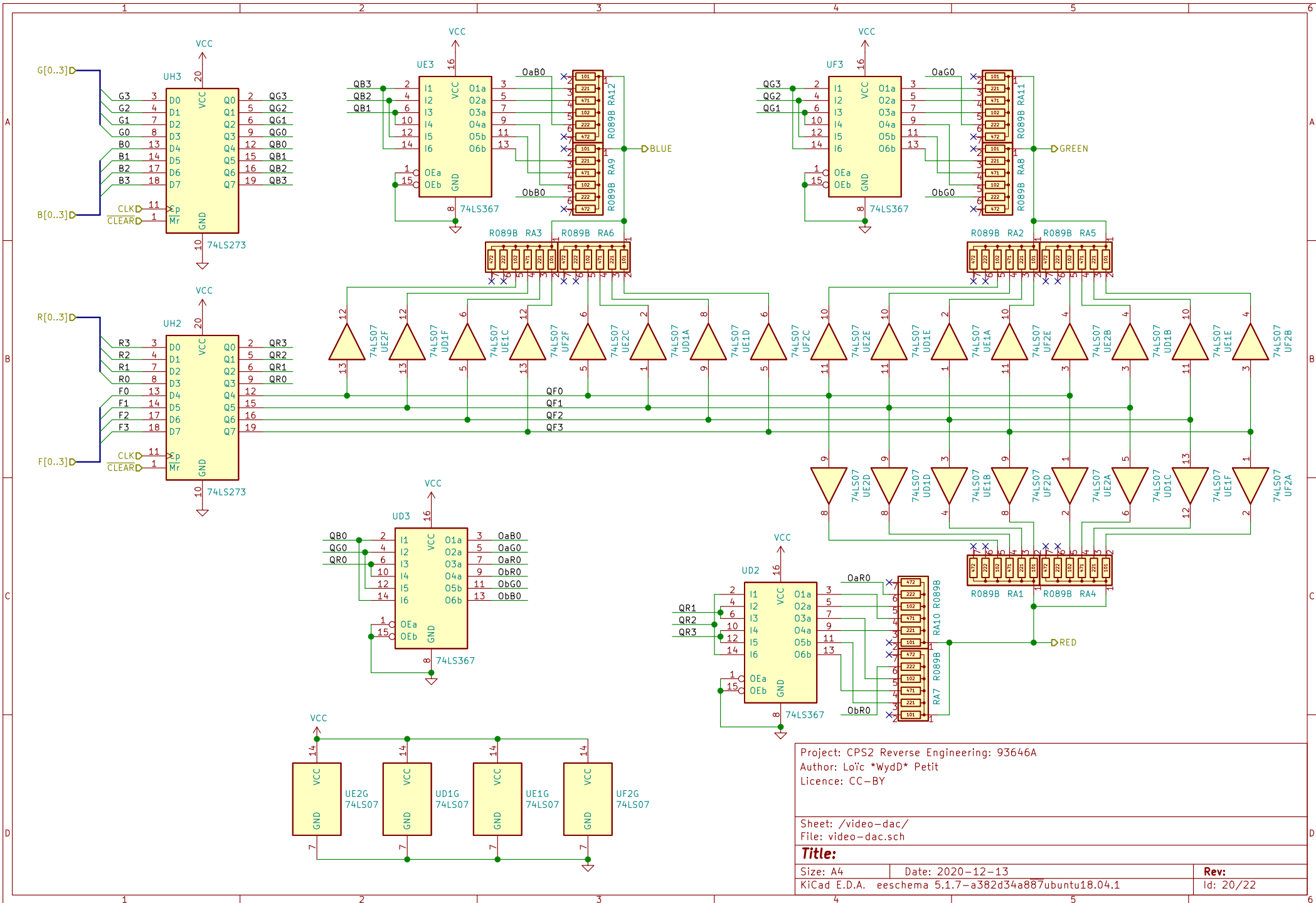


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KiCad E.D.A. eeschema 5.1.7-a382d34a887ubuntu18.04.1		Id: 19/22



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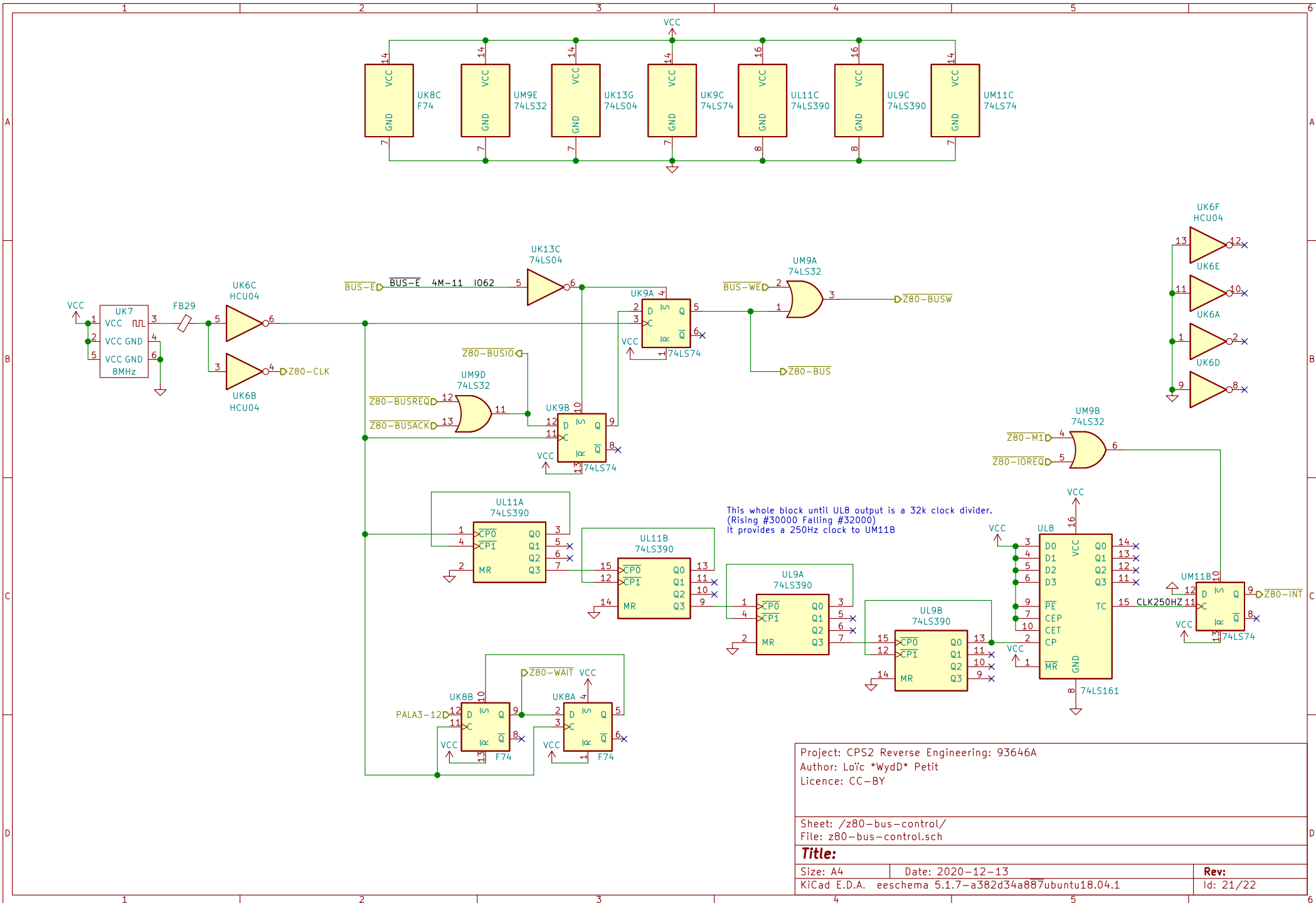
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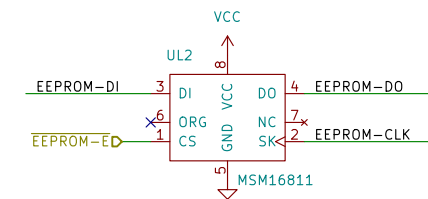
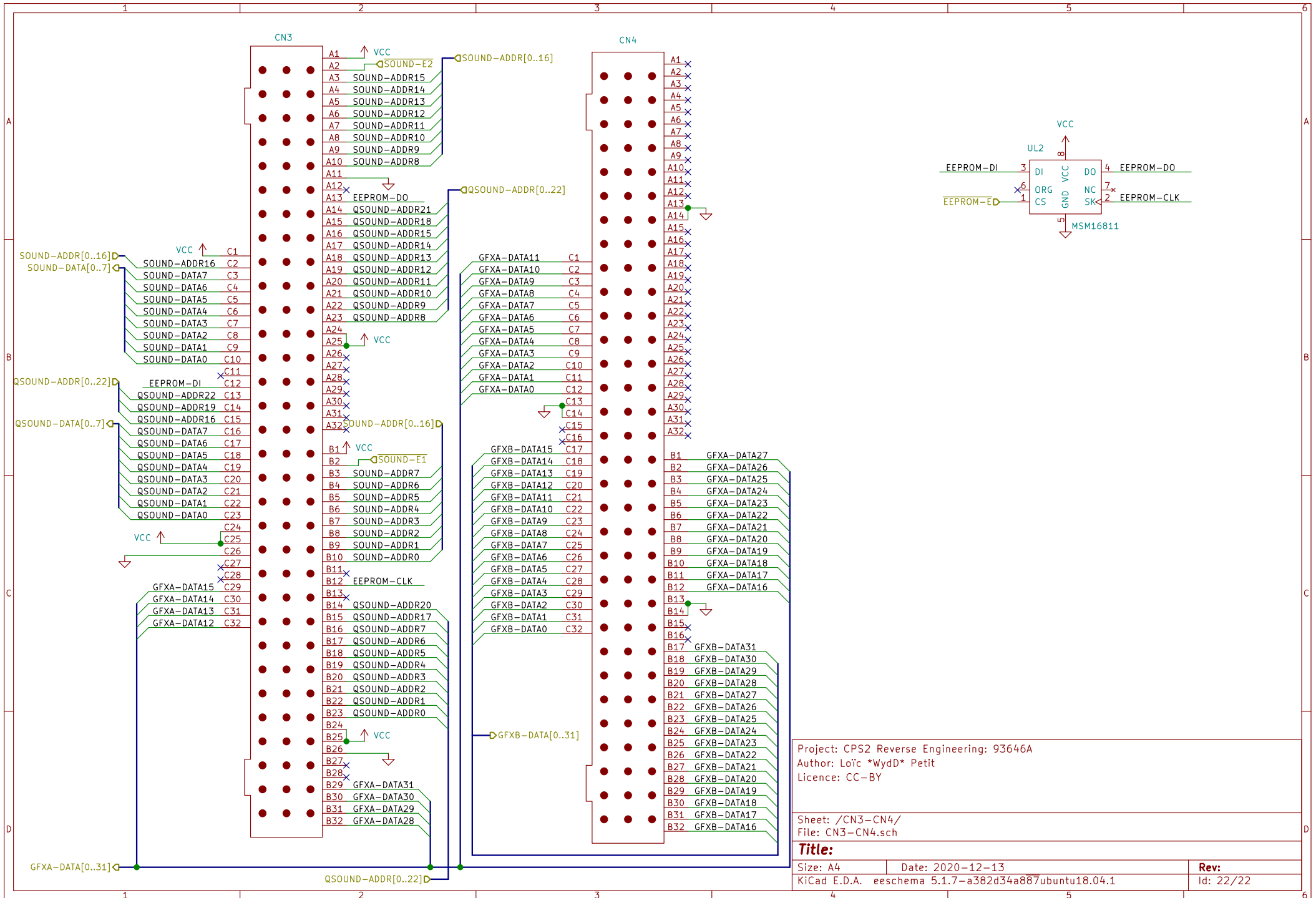
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